



Application of

Inoue et al.

Serial No.: 09/667,706

Group Art Unit: 2811

Filed: September 22, 2000

Examiner: Nguyen, C.Q.

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Honorable Assistant Commissioner of Patents  
Washington, D.C. 20231

RECEIVED  
DEC 21 2001  
TECHNOLOGY CENTER 2800  
#5  
12-22-01  
Payton

**RESPONSE TO FIRST OFFICE ACTION AND RESTRICTION  
REQUIREMENT**

Sir:

In response to the Office Action dated November 21, 2001, please consider the following:

**REMARKS**

In response to the Examiner's restriction requirement, Applicant hereby elects the invention of Group II (e.g., drawn to a method of making a semiconductor device as defined by claims 5-9 and 12-14), without traverse. Applicant reserves the opportunity to file a Divisional Application for the non-elected invention later.

Further, Applicant respectfully requests the Examiner's acknowledgment of the priority document filed on September 22, 2000.

Early, favorable prosecution on the merits is respectfully requested.

Should the Examiner find the application to be other than in condition for allowance, the Examiner is requested to contact the undersigned at the local telephone number listed below to discuss any other changes deemed necessary in a telephonic or personal interview.